




S/N: 10/531,231

**IAP6** Rec'd PCT.PTO 22 FEB 2006 *PCT*

PATENT  
Confirmation No. 4880

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Tajinder MANKU	Examiner:	Unassigned
Serial No.:	10/531,231	Group Art Unit:	Unassigned
Intl. Filing Date:	15 October 2003	Docket:	14964-49625
Title:	DC TRIMMING CIRCUIT FOR RADIO FREQUENCY (RF) DOWN-CONVERSION		

**CERTIFICATE UNDER 37 CFR 1.8:**  
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on February 20, 2006.  
By:   
Name: Tim Tingkang Xia

**TRANSMITTAL**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450


**CUSTOMER NO. 24728**

Sir:

We are transmitting herewith the attached:

- ☒ Transmittal Sheet containing Certificate of Mailing (1 page)
- ☒ Information Disclosure Statement (37 C.F.R. § 1.97(b)) (2 pages)
- ☒ Form PTO-1449 Listing 24 References (2 pages)
- ☒ Copies of Eighteen (18) Articles
- ☒ Return postcard

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By:   
Name: Tim Tingkang Xia  
Reg. No.: 45,242  
TX

S/N 10/531,231

PATENT  
Confirmation No. 4880

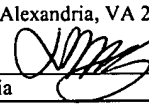


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P.O. Box 1450  
Alexandria, VA 22313-1450

**Customer No. 24728**

**INFORMATION DISCLOSURE STATEMENT (37 C.F.R. § 1.97(b))**

Dear Sir:

With regard to the above-identified application, the items of information listed on the enclosed Form 1449 are brought to the attention of the Examiner.

In accordance with 37 C.F.R. §1.98(a)(2), a copy of each non-U.S. patent document or other information listed on the enclosed Form 1449 is provided herewith, if applicable.

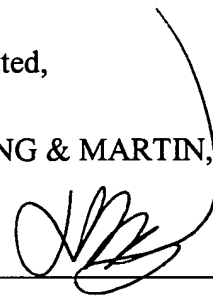
No representation is made that a reference is "prior art" within the meaning of 35 U.S.C. §§ 102 and 103 and Applicants reserve the right, pursuant to 37 C.F.R. § 1.131 or otherwise, to establish that the reference(s) are not "prior art." Moreover, Applicants do not represent that a reference has been thoroughly reviewed or that any relevance of any portion of a reference is intended.

Consideration of the items listed is respectfully requested. Pursuant to the provisions of M.P.E.P. 609, it is requested that the Examiner return a copy of the attached Form 1449, marked as being considered and initialed by the Examiner, to the undersigned with the next official communication.

Respectfully submitted,

MORRIS, MANNING & MARTIN, LLP

February 20, 2006



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Tim Tingkang Xia  
Reg. No. 45,242  
Attorney for Applicant on the Record  
TTX

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## INFORMATION DISCLOSURE STATEMENT

IN AN APPLICATION

(Use several sheets if necessary)

Docket Number: 14934-49625

Serial Number: 10/531,231

Applicant: Tajinder MANKU

Confirmation No.: 4880

Filing Date: 15 October 2003

Group Art Unit: Unassigned

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	1.	6,606,359	08/12/03	Nag et al.			
	2.	4,250,458	02/10/81	Richmond et al.			
	3.	5,375,146	12/20/84	Chalmers			
	4.	5,793,817	08/11/98	Wilson			
	5.	5,548,840	08/20/96	Heck			
	6.	2002/050861 A1	05/02/02	Arnoldus et al.			

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	7.	"Mini-Circuits Modern Mixer Terms Defined," 4 pages (1999).						
	8.	Bradshaw, P. "The ICL7650S: A New Era in Glitch-Free Chopper Stabilized Amplifiers," <i>Application Note</i> , Vol. AN053.2, pp. 1-14, July (2001).						
	9.	Au, T. et al., "Improved Flicker Noise Model for Submicron MOSFET Devices," <i>Department of Electrical Engineering and Computer Sciences, University of California at Berkeley</i> , pp 1-6.						
	10.	Leenaerts, D. "Integrated Transceiver Design, Non-Linear Dynamic Issues," Phillips Research. 34 pages.						
	11.	Kim, B. et al., "Single-Ended Differential RF Circuit Topologies Utilizing Complementary MOS Devices," <i>Journal of Semi-Conductor Technology and Science</i> , Vol. 2, No. 1, pp. 7-18, March (2002).						
	12.	Consandinou, T., et al. "An Auto-Input-Offset Removing Floating Gate Psuedo-Differential Transconductor," <i>EEE Dept., Imperial College of Science, Technology and Medicine</i> , London, 4 pages.						
	13.	Valero, A. et al., "Direct Conversion Receiver Implementation Issues," <i>Texas A&amp;M University, Bluetooth Meeting</i> , 18 pages, March (2000).						
	14.	Luh, L. et al., "A Continuous-Time Common Mode Feedback Circuit (CMFB) for High-Impedance Current Mode Application," <i>Department of Electrical Engineering, University of Southern California</i> , 4 pages.						

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.

[illegible]

**EXAMINER**

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.